

1 **51550/LTR/B600**

WHAT IS CLAIMED IS:

- 5 1. A digital to analog converter (DAC) comprising:
 a first bias source having a potential of one
polarity;
 a second bias source having a potential of the other
polarity;
10 a bistable latch biased by the sources, the latch
having first and second complementary terminals;
 first and second complementary data decoders;
 means for alternately connecting one of the data
decoders to the first bias source while the other data
15 decoder floats; and
 first and second transfer switches coupling the
first and second decoders to the first and second terminals,
respectively, responsive to timing pulses.

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